

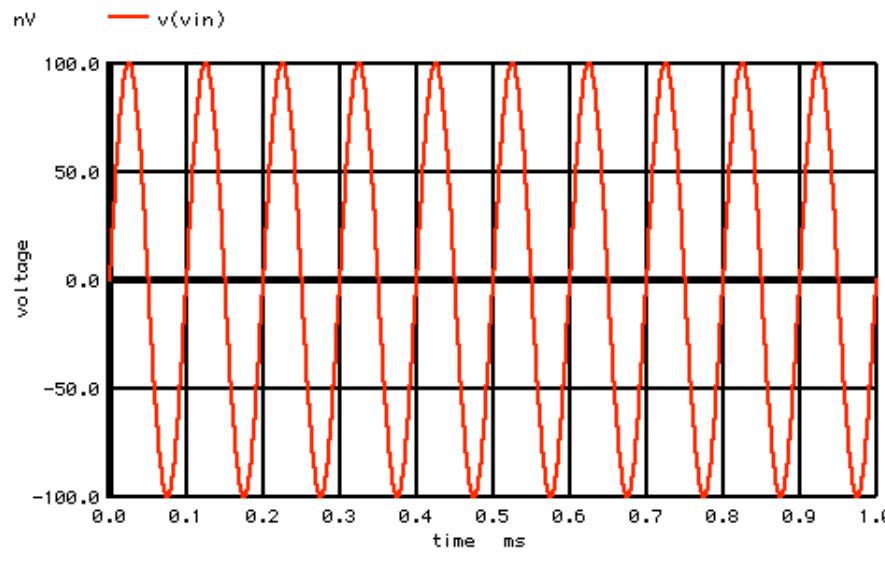
Gain of 100dB

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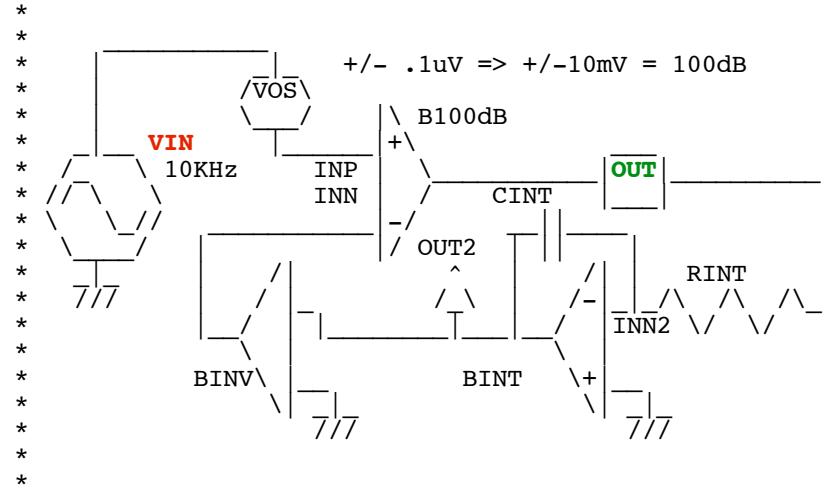
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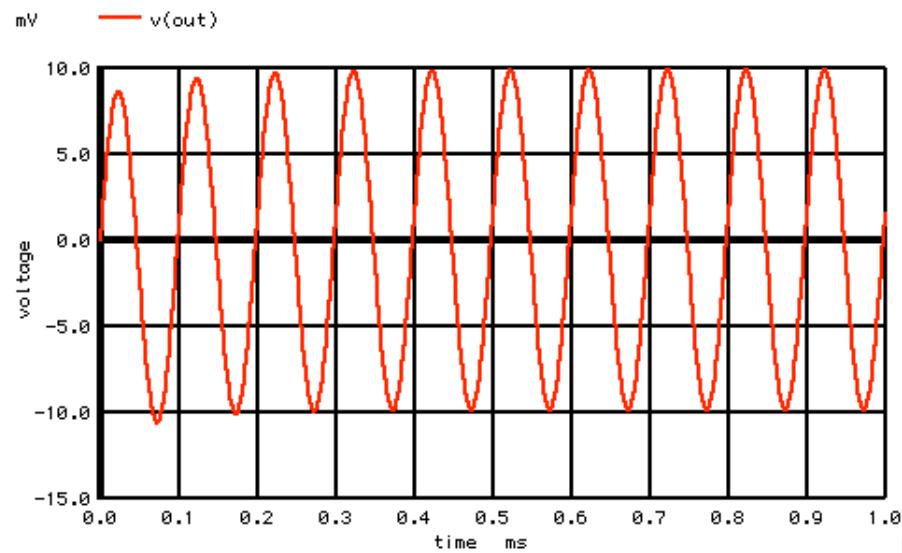
Instead of using an RC network to handle the DC offset, what if an integrator circuit is applied instead?



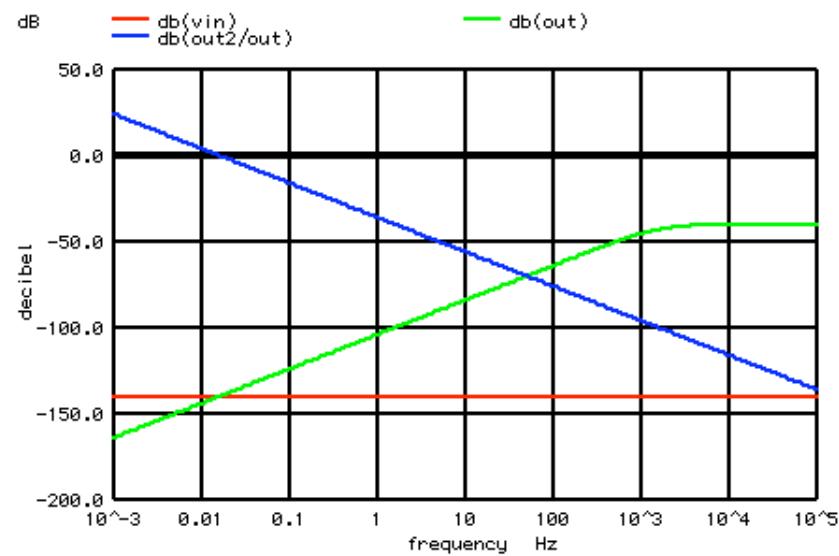
The advantage to the integrator is that it can have a much higher gain than one at DC.



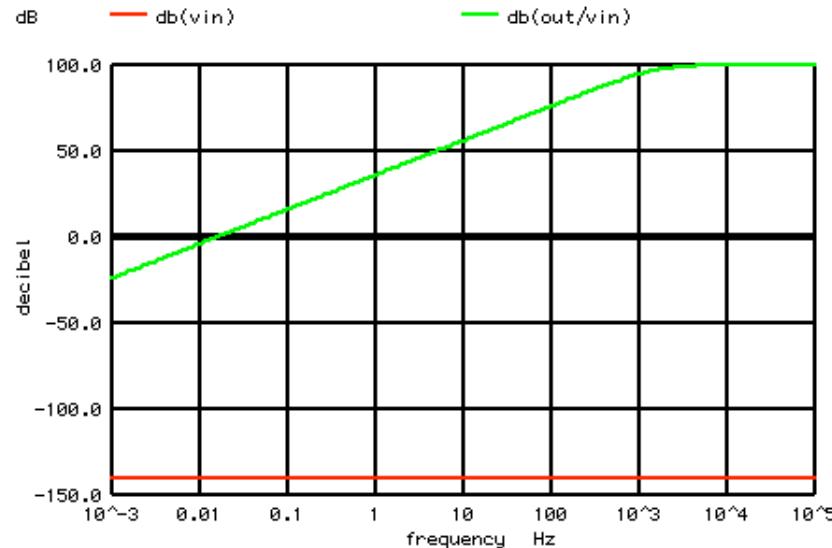
So when the amplifier settles, there can be much attenuation of the input offset present at the output.



The signal magnitudes are shown below. The gain of the integration block is shown to have unity gain at around 10mHz and increase at lower frequencies.



Thinking in terms of this just being an audio frequency amplifier, it will have unity gain at around 10mHz and will flatten out at a gain of 100dB above 1kHz.



Now all one needs to do is to build such an integrator block completely inside the chip.

